

TUNER OF THE TYPE HAVING ZERO INTERMEDIATE FREQUENCY  
AND CORRESPONDING CONTROL PROCESS

Abstract of the Disclosure

A tuner includes an analog block, a digital block, and an analog/digital conversion stage connected therebetween. The analog block includes a first 5 attenuator/controlled-gain amplifier stage connected upstream to a frequency transposition stage. The overall mean power of the entire signal received by the tuner is calculated during a phase of initialization. This overall calculated power is compared in the 10 digital block with a first predetermined reference value corresponding to a maximum power desired at a predetermined location of the analog block. The gain of the first attenuator/amplifier stage is adjusted to minimize the deviation between the overall calculated 15 power and the reference value. In a phase of normal operation, one of the channels of the signal received is selected, with the gain of the first attenuator/amplifier stage being fixed.

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